



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Sreenivasan K. Koduri, et al.

Art Unit: TBD

Serial No.: 10/006,576

Examiner: TBD

Filed: 12/06/01

Docket: TI-33165

For: Back Side Coating of Semiconductor Wafers

COPY OF PAPERS  
ORIGINALLY FILED

LETTER TO THE OFFICIAL DRAFTSPERSON

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)  
I hereby certify that the above correspondence is being deposited  
with the U.S. Postal Service on 2-18-02  
as First Class Mail in an envelope addressed to: Assistant  
Commissioner for Patents, Washington, D.C. 20231.

Karen Vertz 2-18-02  
Karen Vertz Date

Assistant Commissioner for  
Patents  
Washington, D. C. 20231

Dear Sir:

Enclosed are **TWO (2)** sheets of formal drawings for the above-referenced case.  
Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments  
Incorporated. This sheet is enclosed in triplicate.

Texas Instruments Incorporated  
P.O. Box 655474 M/S 3999  
Dallas, Texas 75265  
(214) 939-8651

Respectfully submitted,

Gary C. Honeycutt  
Gary C. Honeycutt  
Reg. No. 20,250  
Attorney for Applicants

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UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov

APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/006,576	12/06/2001	Sreenivasan K. Koduri	TI-33165

23494  
TEXAS INSTRUMENTS INCORPORATED  
P O BOX 655474, M/S 3999  
DALLAS, TX 75265

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CONFIRMATION NO. 3487

FORMALITIES LETTER



\*OC000000007376475\*

Date Mailed: 01/28/2002

**NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION**

FILED UNDER 37 CFR 1.53(b)

*Filing Date Granted*

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is unsigned.
- To avoid abandonment, a late filing fee or oath or declaration surcharge as set forth in 37 CFR 1.16(l) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.
- The balance due by applicant is \$ 130.

*A copy of this notice MUST be returned with the reply.*

*W. Deibel*

Customer Service Center  
Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

03/07/2002 MBERHE 00000102 200668 10006576

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Sreenivasan K. Koduri, et al.

Serial No: 10/006,576

Filed: 12/06/01

For: Back Side Coating of Semiconductor Wafers

Docket No.: TI-33165

Art Unit: 2812

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Assistant Commissioner for

Patents

Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service on 2-18-02 as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C.

Karen Vertz 2-18-02  
Karen Vertz Date

**RESPONSE TO NOTICE TO FILE MISSING PARTS OF APPLICATION**

Dear Sir:

In response to the Notice of Missing Parts dated January 28, 2002 please charge the fee of \$130.00 for the surcharge under 37 CFR 1.27 to Deposit Account No. 20-0668 of Texas Instruments Incorporated.

Please find enclosed the Declaration/Power of Attorney (2 pages), an Assignment with Form PTO 1595 and formal drawings for the above-referenced case. The Commissioner is hereby authorized to charge any additional fees which may be required. **This sheet is submitted in triplicate.**

Respectfully submitted,

Gary C. Honeycutt  
Gary C. Honeycutt  
Attorney for Applicants  
Reg. No. 20,250

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Dallas, TX 75265  
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**TEXAS  
INSTRUMENTS**  
Dallas Texas

03 9074 MS 3999

GCH/Liv



*Box Missing Parts*  
Assistant Commissioner For Patents  
Washington, D.C. 20231

TH-28823-A